

In the Claims

CLAIMS

Claims 1-54 (Cancelled).

Claims 55-68 (Withdrawn).

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69. (Currently Amended) A semiconductor construction comprising:  
a semiconductor substrate having a trench extending partially therein and  
upper surfaces adjacent the trench;  
an oxide layer formed over the upper surfaces of the semiconductor  
substrate and having an uppermost surface; and  
an insulative material filling the trench and having a portion outward of the  
trench and semiconductor substrate, the portion comprising an outermost upper  
surface elevationally above the uppermost surface of the oxide layer and  
sidewalls connecting the outermost upper surface with the oxide layer, the  
connection between the sidewalls and the outermost upper surface comprising  
first curved corners, and the connection between the sidewalls and the oxide  
layer comprising second curved corners extending from elevationally above the  
oxide layer downward to the uppermost surface of the oxide layer.

70. (Previously Added) The semiconductor construction of claim 69  
wherein the insulative material comprises oxide.

71. (Previously Added) The semiconductor construction of claim 69 wherein the insulative material comprises a first insulative material partially filling the trench and a second insulative material formed over the first insulative material.

Concluded  
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72. (Previously Added) The semiconductor construction of claim 69 further comprising a polysilicon layer formed against the uppermost surface of the oxide layer and the portion of the insulative material.

73. (New) The semiconductor construction of claim 69 wherein the trench comprises sidewalls connected by a bottom wall, and wherein the first curved corners of the portion are elevationally above and between the sidewalls of the trench.

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74. (New) The semiconductor construction of claim 69 wherein the trench comprises sidewalls connected by a bottom wall, and wherein the first curved corners of the portion are directly over the bottom wall of the trench.